FIG. 1

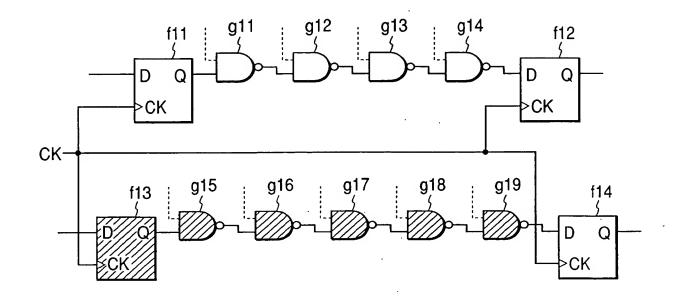
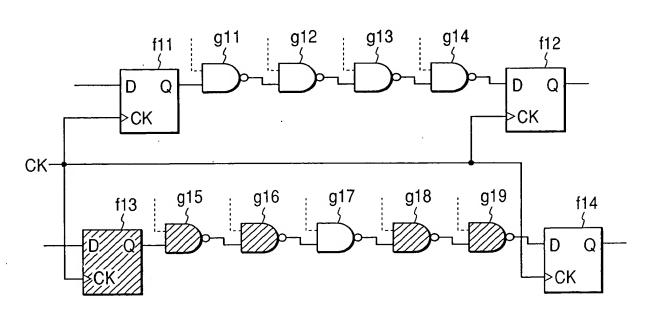


FIG. 2



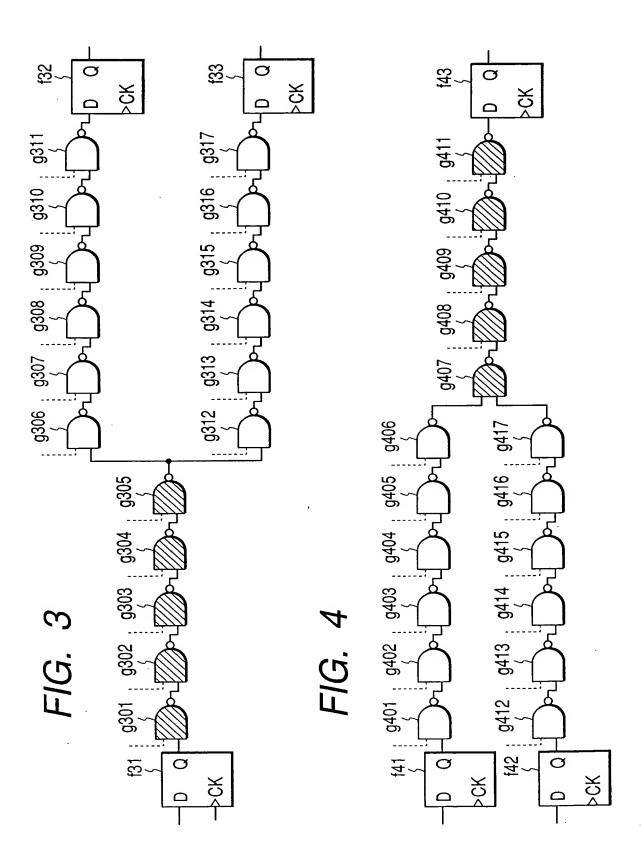
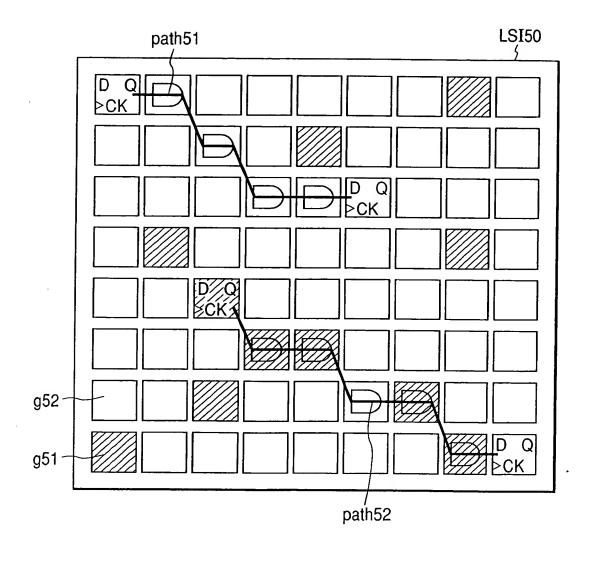
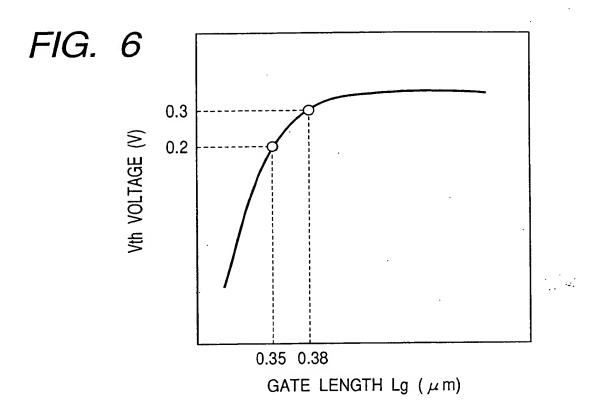


FIG. 5





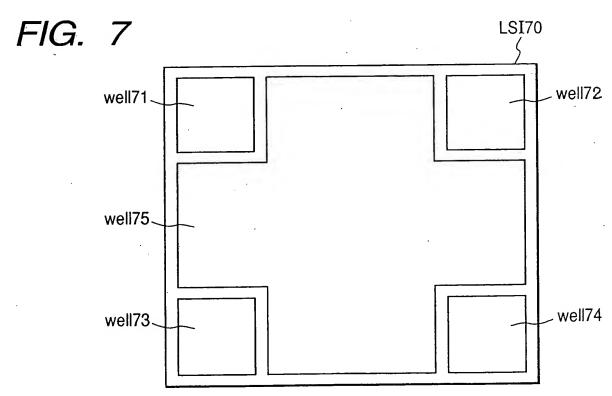
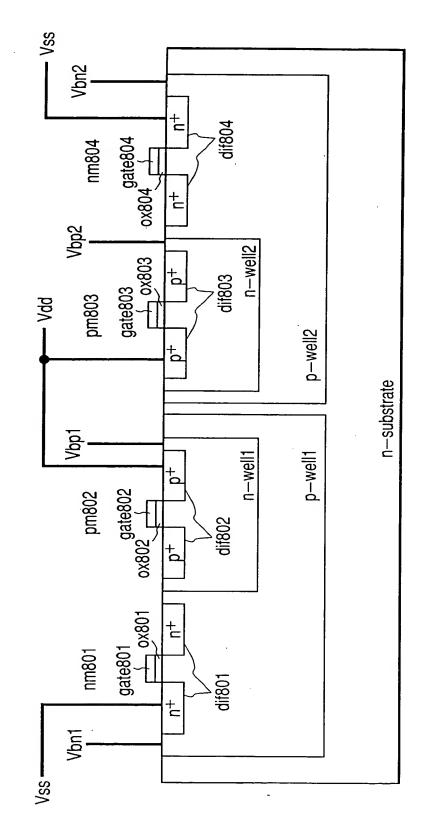
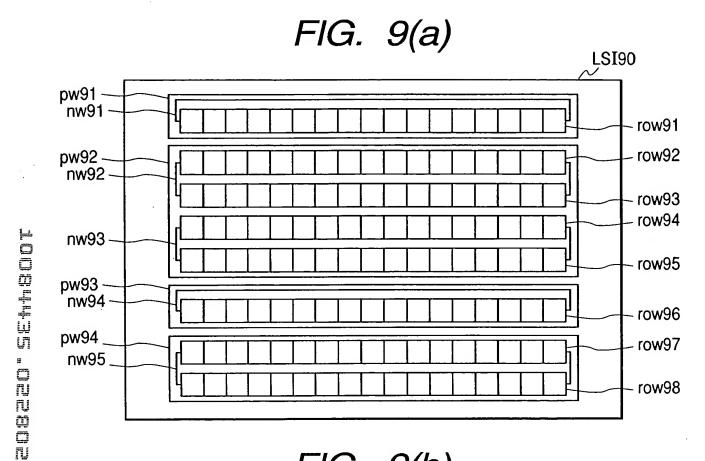
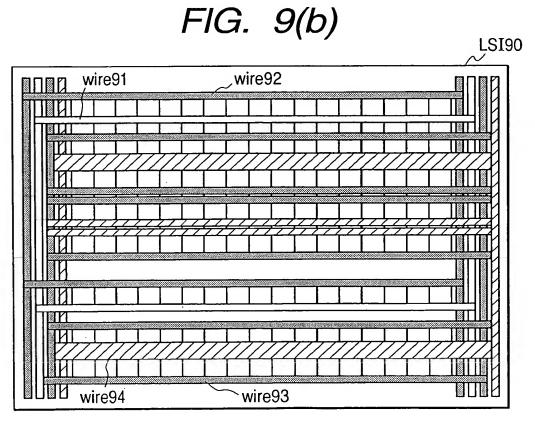


FIG. 8







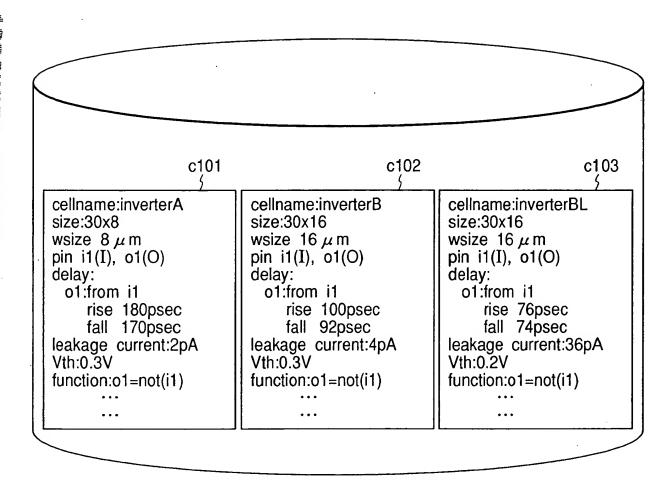


FIG. 11

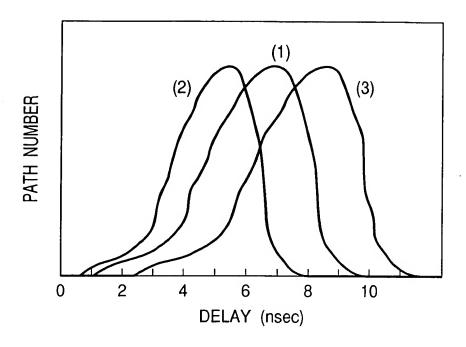


FIG. 12

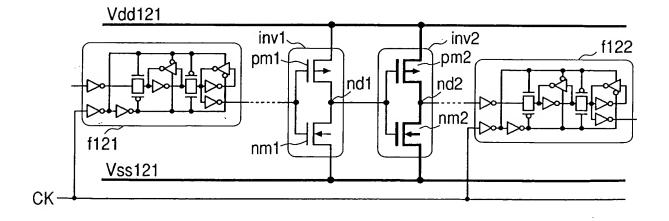
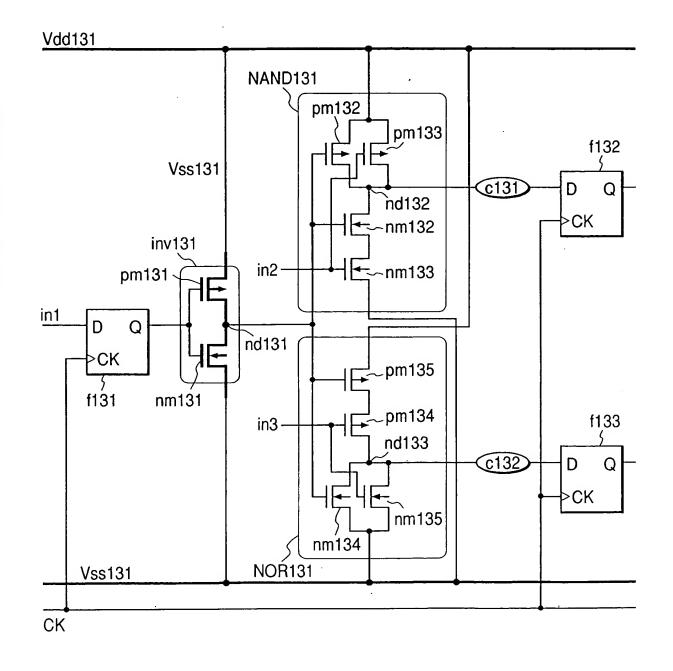
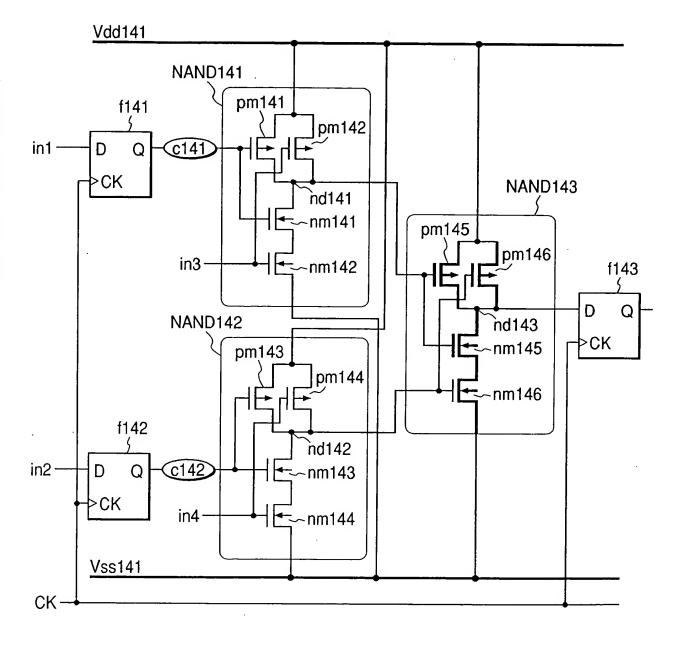
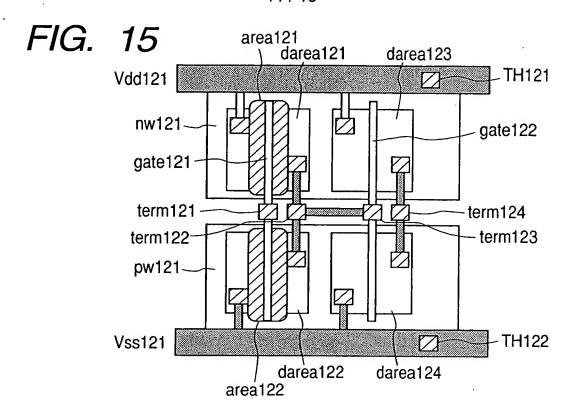


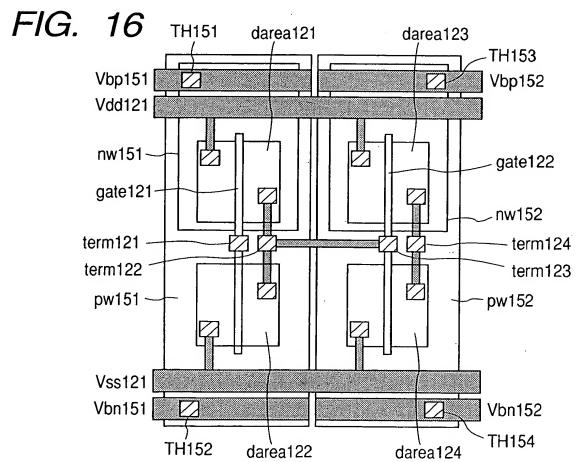
FIG. 13

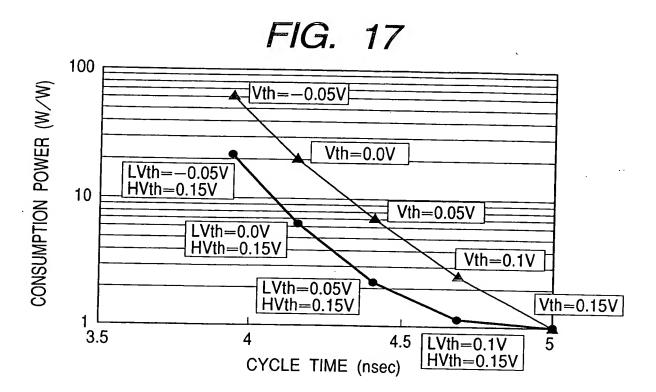


TOOSH+WS CRESCE









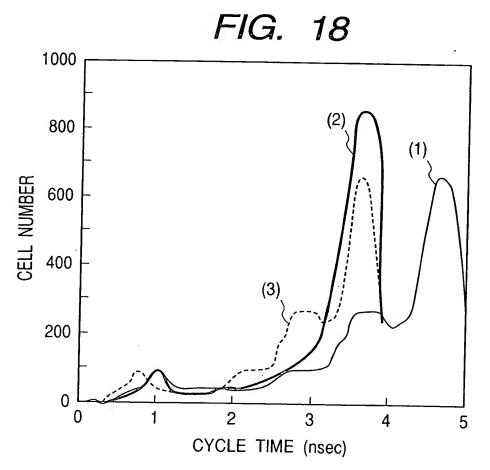


FIG. 19

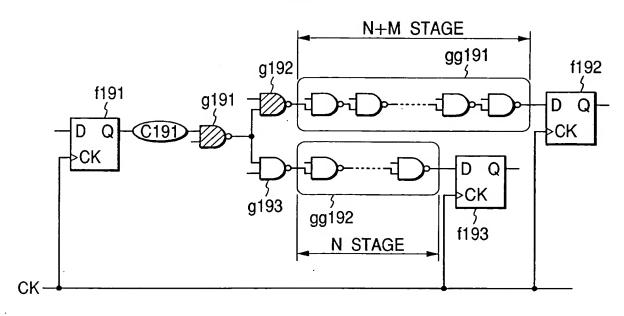
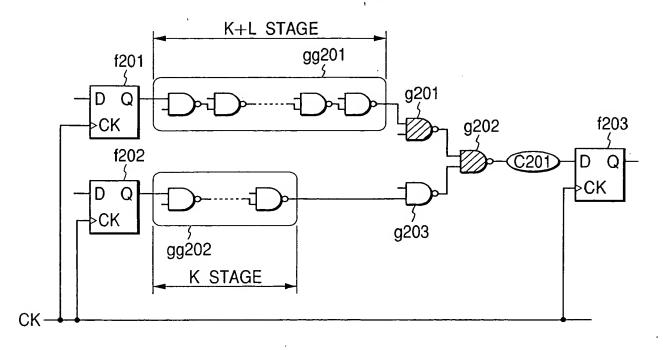


FIG. 20



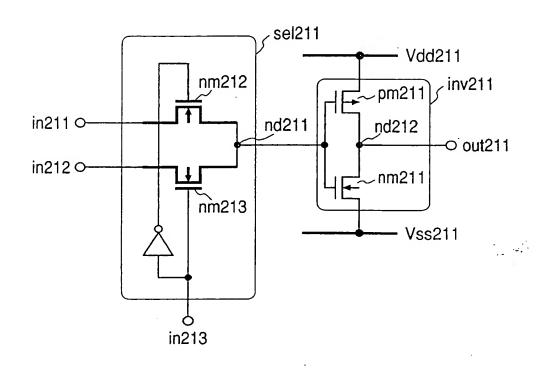
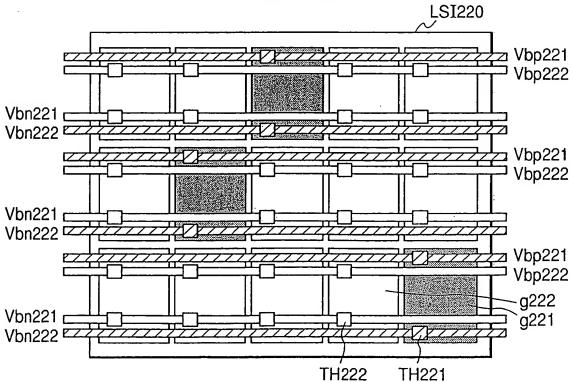
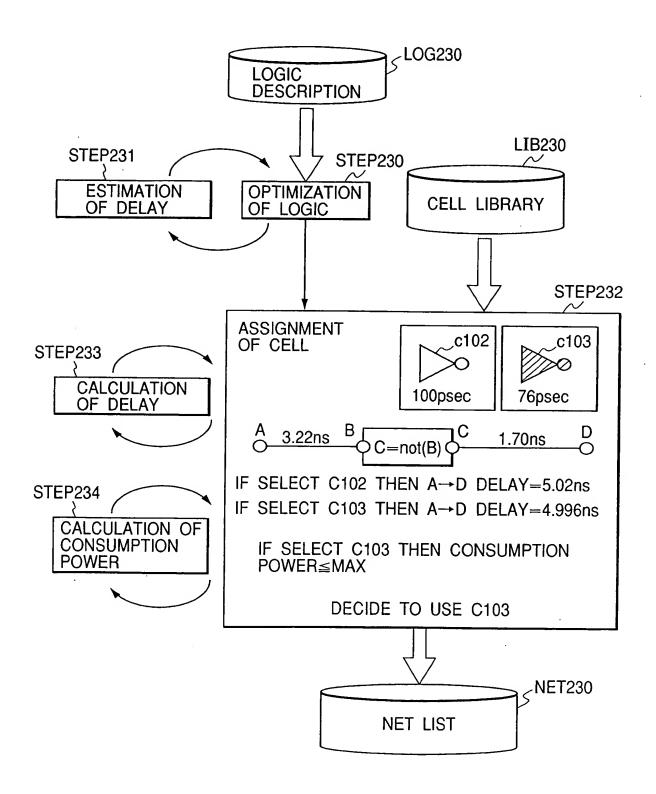
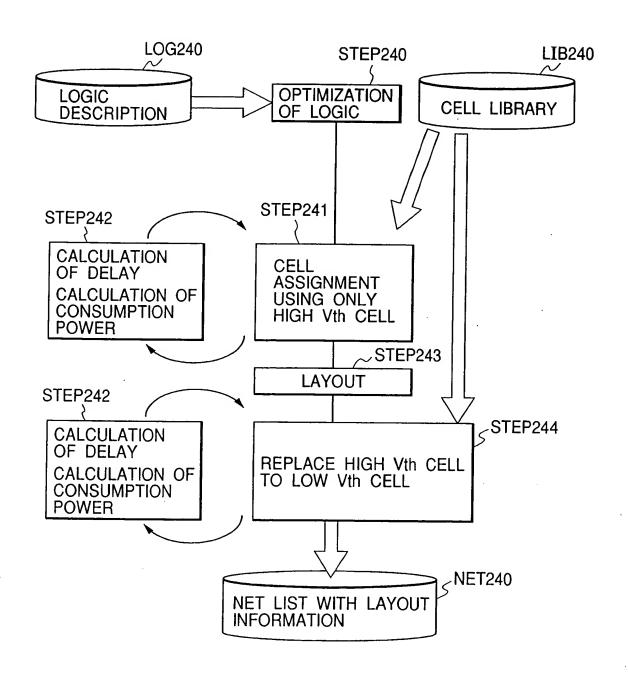


FIG. 22







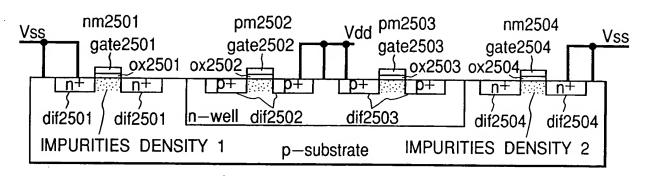
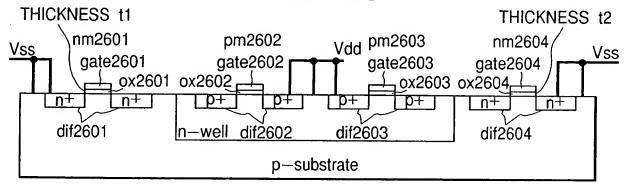
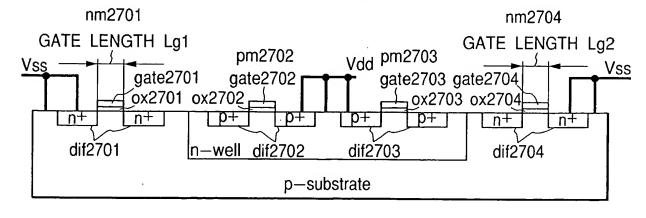


FIG. 26





FPU	BSC
	CASH
	CPU
DMAC	INT CPG